

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	95951	(carrier adj module\$2 circuit adj board\$2 multichip\$2 chip\$2 "ic" semiconductor adj device\$2 integrated adj circuit die) with(hous\$4 cover\$4)	US-PGPUB; USPAT	OR	OFF	2005/02/14 16:58
L2	146	((pass)adj2 (via\$2 hole\$2 window\$2 opening\$2))near11(communitc\$4 contact\$4 coupl\$4)and 1	US-PGPUB; USPAT	OR	OFF	2005/02/14 16:50
L3	146	((pass)adj2 (via\$2 hole\$2 window\$2 opening\$2))near11(communitc\$5 contact\$4 coupl\$4)and 1	US-PGPUB; USPAT	OR	OFF	2005/02/14 16:59
L4	169	((pass)adj2 (via\$2 hole\$2 window\$2 opening\$2))near11(communicat\$5 contact\$4 coupl\$4)and 1	US-PGPUB; USPAT	OR	OFF	2005/02/14 16:50
L5	23	4 not 3	US-PGPUB; USPAT	OR	OFF	2005/02/14 16:53
L6	5	(socket\$4 near2 test\$4)and 4	US-PGPUB; USPAT	OR	OFF	2005/02/14 16:59
L7	134888	(carrier adj module\$2 circuit adj board\$2 multichip\$2 chip\$2 "ic" semiconductor adj device\$2 integrated adj circuit die) and(hous\$4 cover\$4)	EPO; JPO; DERWENT	OR	OFF	2005/02/14 16:59
L8	216	(socket\$4 near2 test\$4)and 7	EPO; JPO; DERWENT	OR	OFF	2005/02/14 16:59
L9	0	((pass)adj2 (via\$2 hole\$2 window\$2 opening\$2))and 8	EPO; JPO; DERWENT	OR	OFF	2005/02/14 17:00
L10	73	(via\$2 hole\$2 window\$2 opening\$2)and 8	EPO; JPO; DERWENT	OR	OFF	2005/02/14 17:02
L11	2	(via\$2 hole\$2 window\$2 opening\$2)and 8	EPO	OR	OFF	2005/02/14 17:04
L12	2	(via\$2 hole\$2 window\$2 opening\$2)and 8 and tub\$4	JPO; DERWENT	OR	OFF	2005/02/14 17:06
L13	0	12 and latch\$2	JPO; DERWENT	OR	OFF	2005/02/14 17:06